

# Om Prakash Singh

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## EXPERIENCE

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### **L&T Technology Services, Bangalore-** *Associate Engineer*

September 2018 - PRESENT

- Development of validation plans ,Coding and Debug of scenarios on Pre and Post Silicon platform
- Pre and post- Silicon validation on high performance microcontroller SOCs
- Driving a team for Lab Bring-Up for post Silicon

### **National Institute of Technology, Durgapur-** *Graduate Research Assistant*

July 2016 - May 2018

- Working on Intelligent Transport System Project with IIT Bombay, IIT Kharagpur and Ministry of Transport, Govt. Of India
- Supervised Undergraduate final year students for research Projects.
- Supported department members with administrative, research and academic assistance.
- Responsible for programming labs for Undergraduate students.

## EDUCATION

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- **M.Tech** [2018] in **CSE** from **National Institute of Technology Durgapur**
- **B.E** [2015] in **CSE** from **Rajiv Gandhi Prodyogiki Vishwavidyalaya Bhopal**
- **12th** [2011] from **HCYIC, Ghazipur, U.P**
- **10th** [2008] from **Central Hindu Boys School Varanasi**

## PROJECTS

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- **Pre-Silicon Validation**
  - Bring-up of Pre Silicon Validation platforms like Palladium or FPGA for the SOC
  - Bring-up of Silicon Validation platforms like Validation Board & Silicon (EVB , MUB) for new SOC design
  - Creating and performing system use scenarios
  - Validation of Storage products complaint of NVMe Spec using FPGA and debugging using JTAG
  - Working Closely with FW, ASIC and HW teams for reporting failures and debugging.
  - Reproducing the test failure scenario with help of internal tools and development of test cases for the failure.

Duration: July 2019 – Present

- **NVMe Protocol Testing**
  - Involved in testing of NVMe devices based on 1.3 spec with help of OakGate, Lecroy analyzer and Exerciser and Python Scripts.
  - Responsible for writing and execution of test cases developed in C and Python.
  - Analyzing PCIe traces with Lecroy analyzer and Lecroy exerciser.

Duration: Jan 2019 – June 2019

- **Detection and classification of Road surface anomalies**
  - Acquiring road surface data and then applying ML algorithms for detection and classification of different road surface events and classifying them based on the feature set obtained from data.
  - Collected accelerometer sensors data from Mobiles phones inbuilt sensors and then applied feature engineering and using Logistic regression to train the model for detection and classification.

Duration: Aug 2017 – May 2018

## SKILLS

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- Good problem Solving and debug skills
- Knowledge of C and C++ for embedded development
- Experience in Scripting and test Suite Development using Python.
- FPGA Validation
- JTAG/Metaware Debugging
- Test equipment like Logic analyzer, Oscilloscope and Protocol analyzers.
- NVMe Over PCIe, I2C, SPI communication protocol Knowledge

## ACHIEVEMENTS

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- Awarded Star of the Month for Feb 2020.
- Awarded for contribution towards POC based on PCIe.
- Developed and automated trace reader and reduced time of analyzing failure traces from 5 hours to 30 minutes.